FIGURE 1A

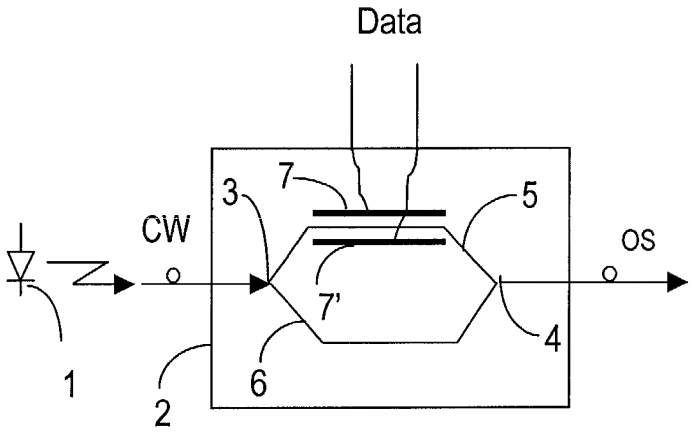


FIGURE 1B

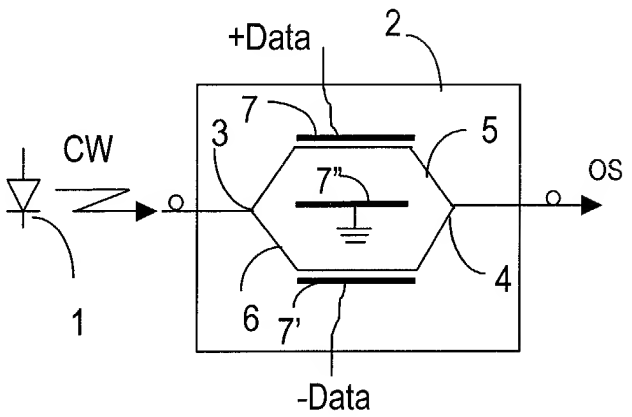
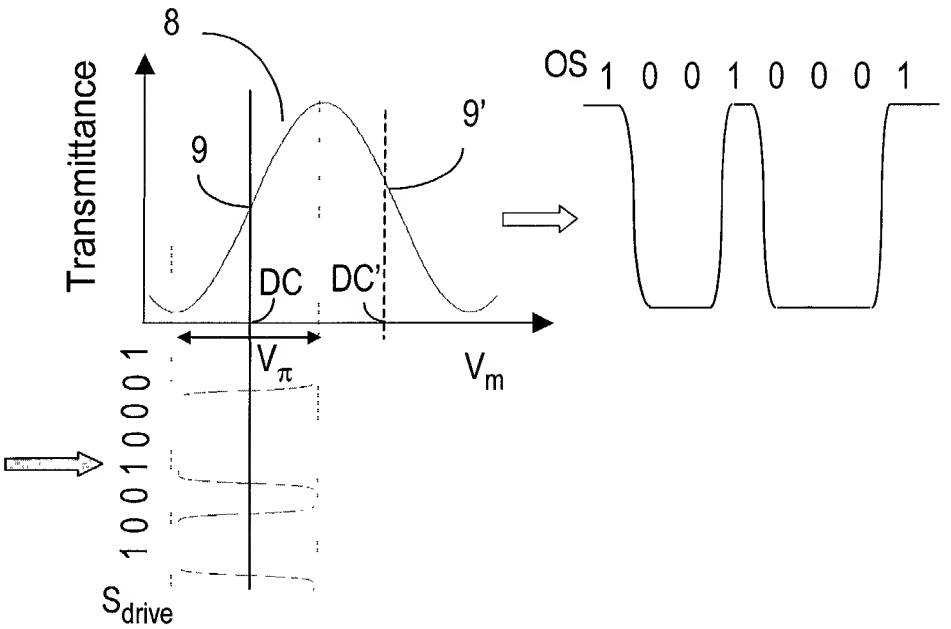


FIGURE 1C



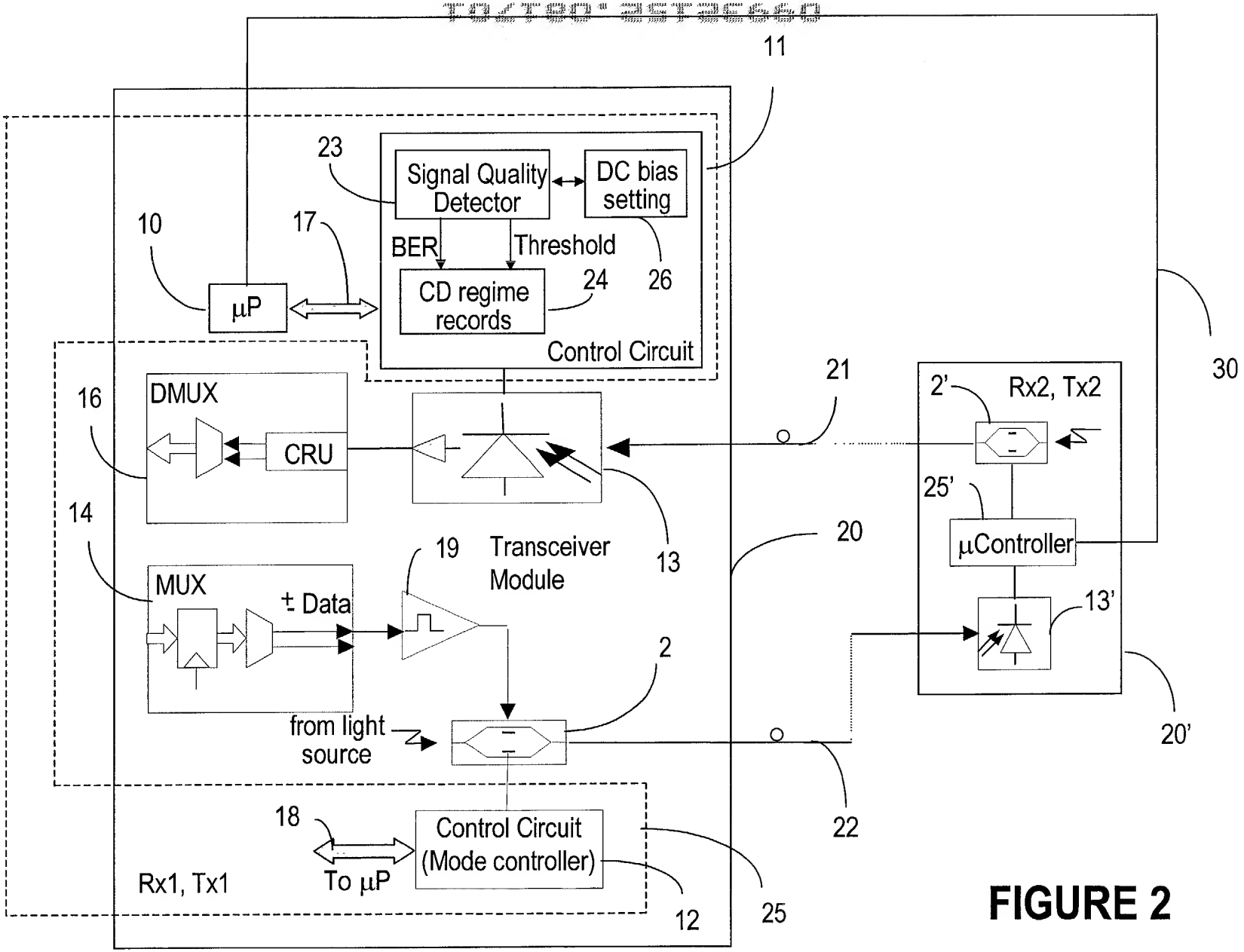

11
23
Signal Quality Detector
DC bias setting
10
17
BER
Threshold
24
26
μP
CD regime records
Control Circuit
30
21
2'
Rx2, Tx2
16
DMUX
CRU
25'
μController
14
19
Transceiver Module
20
13
MUX
± Data
13'
2
from light source
20'
22
18
Control Circuit (Mode controller)
Rx1, Tx1
To μP
12
25

FIGURE 2